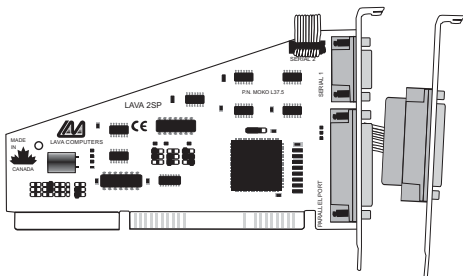


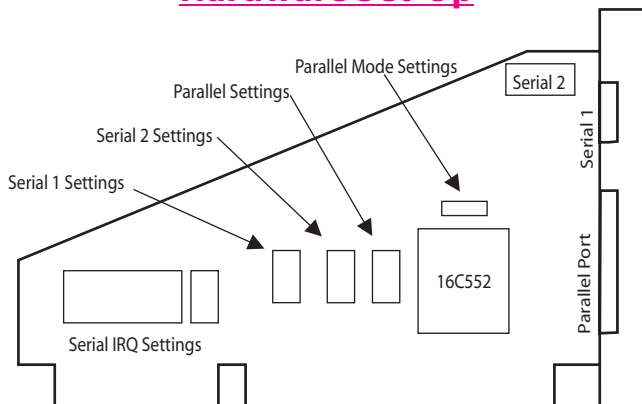
# Lava 2SP-550 Installation Manual

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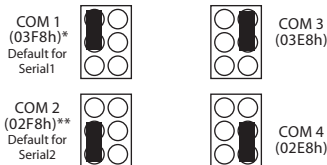


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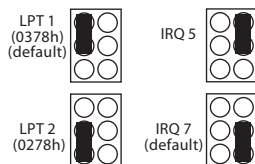
# Hardware Set-Up



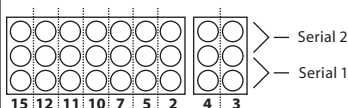
## Serial Settings



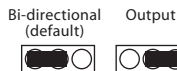
## Parallel Settings



## Serial IRQ Settings



## Parallel Mode Settings



# 2SP-550 Configuration

## Serial Configuration

1. Determine the number of serial ports installed in your system. Each serial port will have an unique "COM" designation. Configure the ports of the 2SP-550 for two unused COM addresses.
2. Determine which IRQ's (interrupts) are free in your system. Configure each serial port on the 2SP-550 for an unused IRQ. Although some devices allow sharing of IRQ's, this is not recommended.
- 3a. For Serial 1, install the jumper above the number corresponding to the desired IRQ, so that it covers the pin in the bottom row and the pin in the middle row.
- 3b. For Serial 2, install the jumper above the number corresponding to the desired IRQ, so that it covers the pin in the top row and the pin in the middle row.

**Do not install more than one IRQ jumper per serial port.**

## Parallel Configuration

1. Determine the number of parallel ports installed in your system. Each parallel port will have an unique "LPT" designation. Configure the port of the 2SP-550 for an unused LPT address (e.g. If you have a parallel port installed as LPT 1 using hexadecimal address 0378, configure the 2SP-550 as LPT 2 using address 0278h).
- 2a. Configure the parallel port for an unique IRQ, if required. Many applications do not require an IRQ assignment for operation. Consult the documentation accompanying your software and/or operating system.
- 2b. Typically, a parallel port configured for LPT 1 at address 0378h, will be set for IRQ 7 and a port configured for LPT 2 at address 0278h, will be set for IRQ 5.
3. The 2SP-550 supports two parallel modes: Output (SPP, 4-bit, "nibble") & Bi-directional (8-bit, "byte," PS/2 compatible).

**Note: To disable a port, remove the jumper from both of the IRQ and the corresponding COM or LPT jumper configuration pins.**

# **Lava Technical Support**

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## **Lava Computer MFG Inc.**



This device complies with part 15 of the FCC Rules. Operation is subject to the following conditions: (1) This device may not cause harmful interference, and (2) this device must accept any interference received, including interference that may cause undesired operation.

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